FIG.1

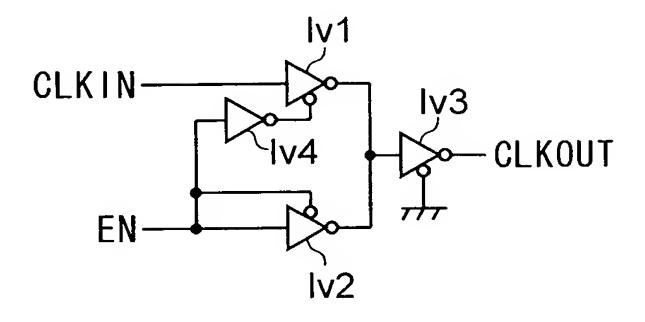


FIG.2

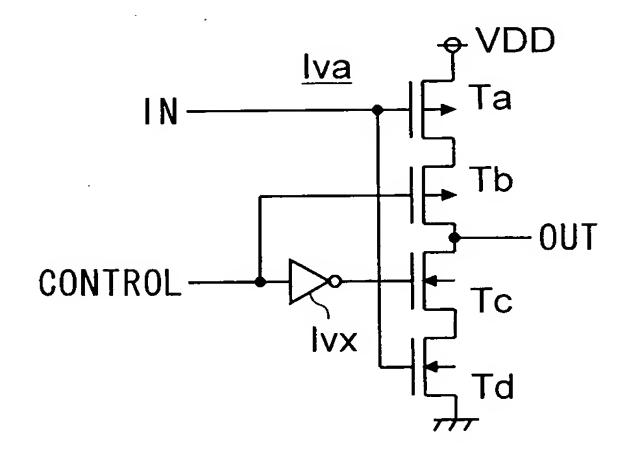


FIG.3A

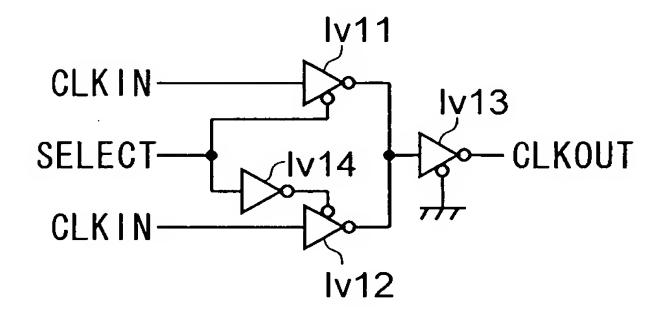


FIG.3B

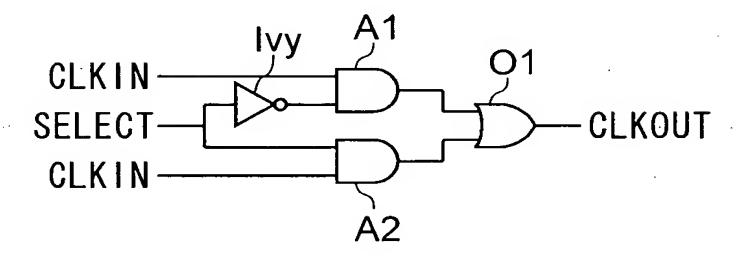


FIG.4

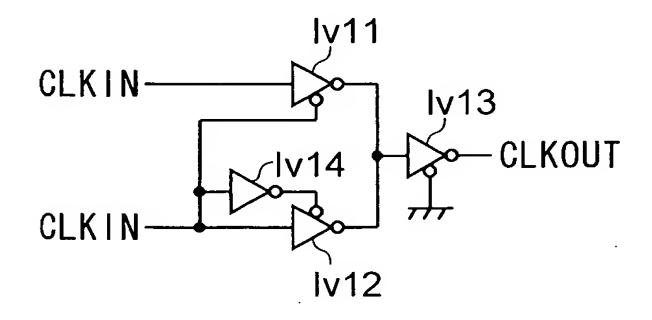


FIG.5

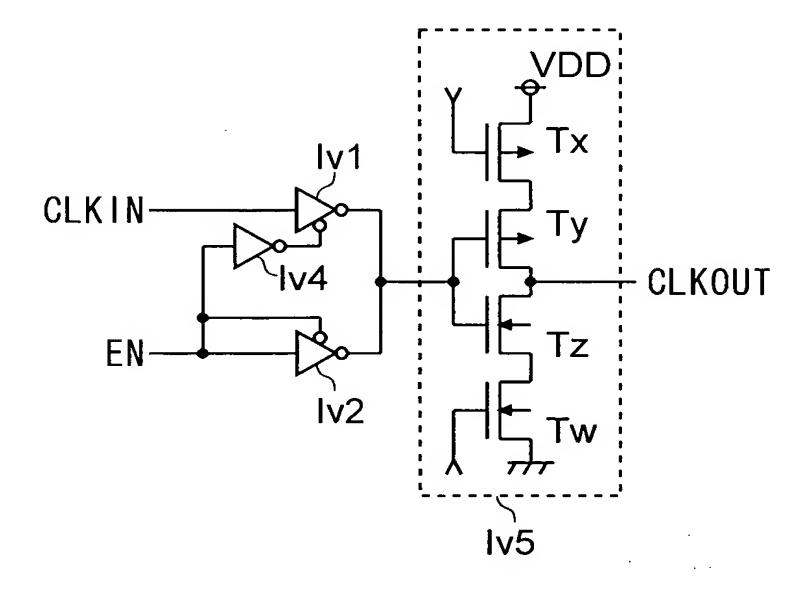
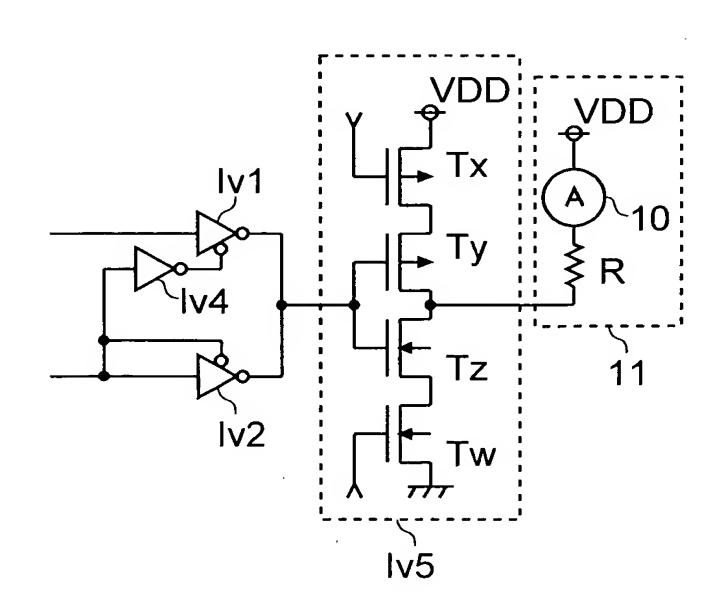


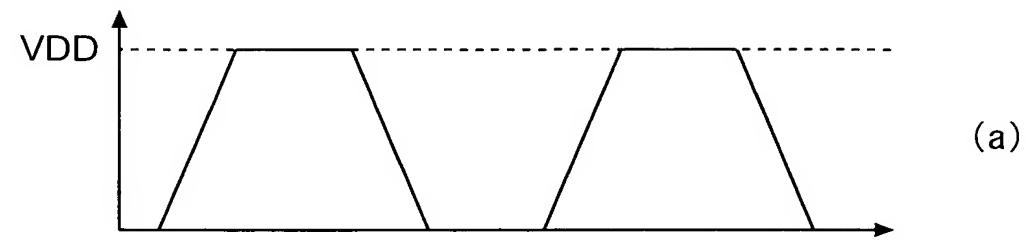
FIG.6

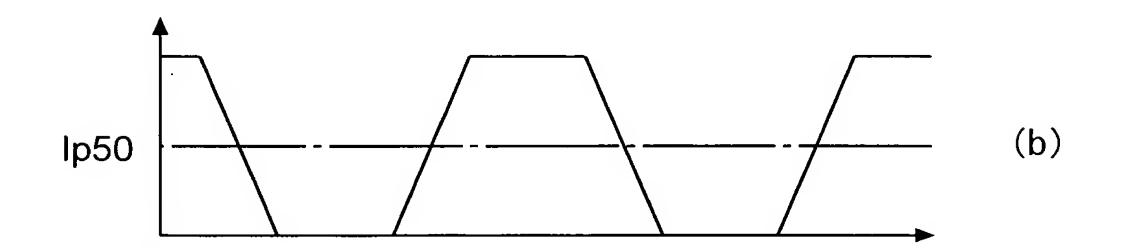


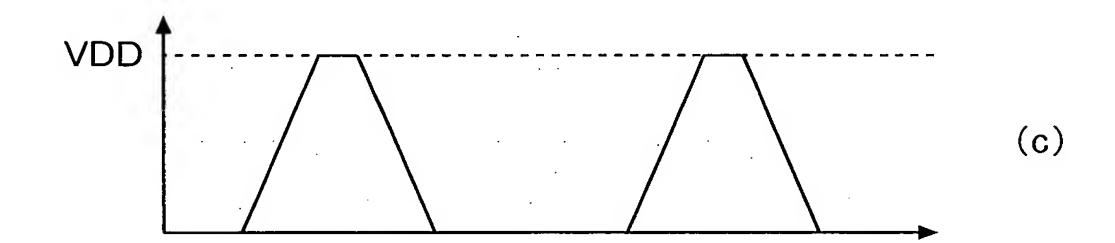
Matter No.: 19415-008US1 Applicant(s): M. Onishi et al. CLOCK INPUT/OUTPUT DEVICE

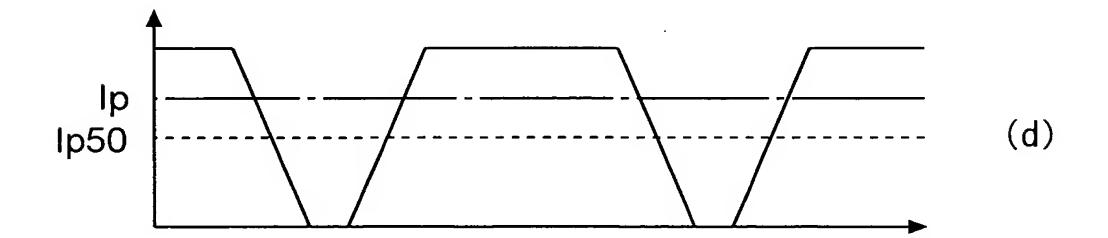
4/6

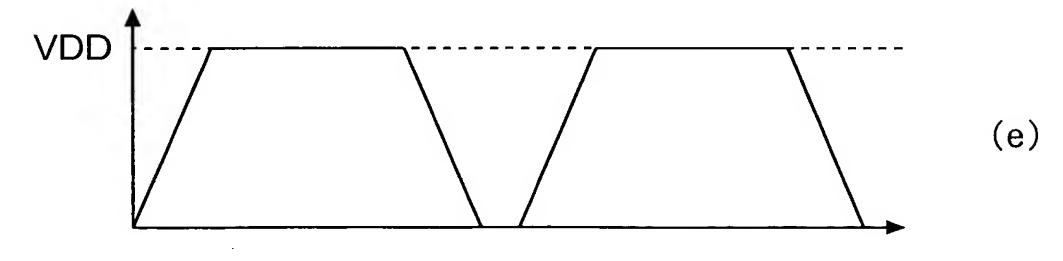












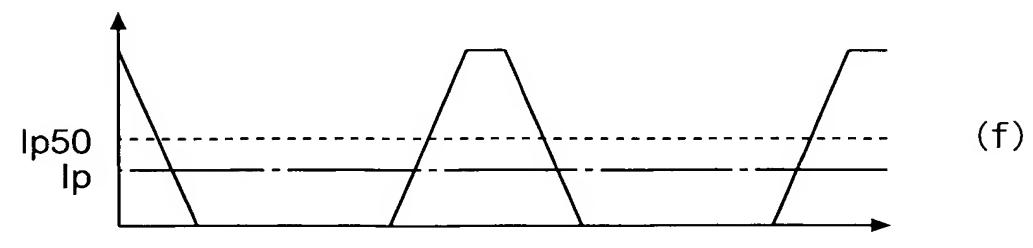
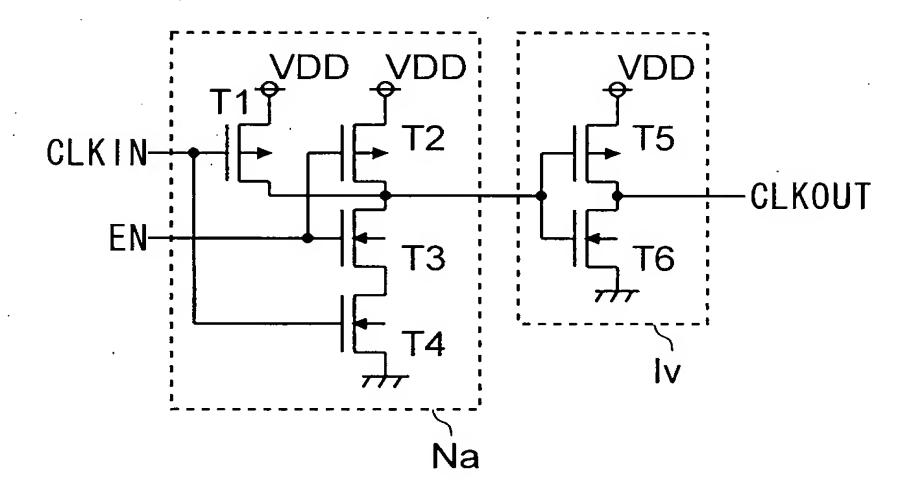


FIG.8

FIG.9



6/6

FIG.10

